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001701.00241TERMINAL DISCLAIMER TO OBLIGATE A DOUBLE PATENTING
REJECTION OVER A PRIOR PATENT

In re Application of: Tamio IKEHASHI et al.

Application No. 10/685,019 10/665019

Filed: September 22, 2003

For: Read Circuit of Nonvolatile Semiconductor Memory

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2. The undersigned is an attorney of record.



5/25/04

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